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Yosef Grodzinsky, Katrin Amunts

Programming Multicore and Many-core Computing Systems Sabri Pllana, Fatos Xhafa, 2017-01-23 Programming multi-core and many-core computing systems Sabri Pllana, Linnaeus University, Sweden Fatos Xhafa, Technical University of Catalonia, Spain Provides state-of-the-art methods for programming multi-core and many-core systems The book comprises a selection of twenty two chapters covering: fundamental techniques and algorithms; programming approaches; methodologies and frameworks; scheduling and management; testing and evaluation methodologies; and case studies for programming multi-core and many-core systems. Program development for multi-core processors, especially for heterogeneous multi-core processors, is significantly more complex than for single-core processors. However, programmers have been traditionally trained for the development of sequential programs, and only a small percentage of them have experience with parallel programming. In the past, only a relatively small group of programmers interested in High Performance Computing (HPC) was concerned with the parallel programming issues, but the situation has changed dramatically with the appearance of multi-core processors on commonly used computing systems. It is expected that with the pervasiveness of multi-core processors, parallel programming will become mainstream. The pervasiveness of multi-core processors affects a large spectrum of systems, from embedded and general-purpose, to high-end computing systems. This book assists programmers in mastering the efficient programming of multi-core systems, which is of paramount importance for the software-intensive industry towards a more effective product-development cycle. Key features: Lessons, challenges, and roadmaps ahead. Contains real world examples and case studies. Helps programmers in mastering the efficient programming of multi-core and many-core systems. The book serves as a reference for a larger audience of practitioners, young researchers and graduate level students. A basic level of programming knowledge is required to use this book.

Languages and Compilers for Parallel Computing Keith Cooper, John Mellor-Crummey, Vivek Sarkar, 2011-03-07 This book constitutes the thoroughly refereed post-proceedings of the 23rd International Workshop on Languages and Compilers for Parallel Computing, LCPC 2010, held in Houston, TX, USA, in October 2010. The 18 revised full papers presented were carefully reviewed and selected from 47 submissions. The scope of the workshop spans foundational results and practical experience, and targets all classes of parallel platforms including concurrent, multithreaded, multicore, accelerated, multiprocessor, and cluster systems.

Digital Video Floriano De Rango, 2010-02-01 This book tries to address different aspects and issues related to video and multimedia distribution over the heterogeneous environment considering broadband satellite networks and general wireless systems where wireless communications and conditions can pose serious problems to the efficient and reliable delivery of content. Specific chapters of the book relate to different research topics covering the architectural aspects of the most famous DVB standard (DVB-T, DVB-S/S2, DVB-H etc.), the protocol aspects and the transmission techniques making use of MIMO, hierarchical modulation and lossy compression. In addition, research issues related to the application layer and to the content semantic, organization and research on the web have also been addressed in order to give a complete view of the problems. The network technologies used in the book

are mainly broadband wireless and satellite networks. The book can be read by intermediate students, researchers, engineers or people with some knowledge or specialization in network topics.

Artificial Neural Networks: Formal Models and Their Applications – ICANN 2005 Wlodzislaw Duch, Erkki Oja, Slawomir Zadrozny, 2005-08-25 This volume is the first part of the two-volume proceedings of the International Conference on Artificial Neural Networks (ICANN 2005), held on September 11–15, 2005 in Warsaw, Poland, with several accompanying workshops held on September 15, 2005 at the Nicolaus Copernicus University, Toru, Poland. The ICANN conference is an annual meeting organized by the European Neural Network Society in cooperation with the International Neural Network Society, the Japanese Neural Network Society, and the IEEE Computational Intelligence Society. It is the premier European event covering all topics concerned with neural networks and related areas. The ICANN series of conferences was initiated in 1991 and soon became the major European gathering for experts in those fields. In 2005 the ICANN conference was organized by the Systems Research Institute, Polish Academy of Sciences, Warsaw, Poland, and the Nicolaus Copernicus University, Toru, Poland. From over 600 papers submitted to the regular sessions and some 10 special conference sessions, the International Program Committee selected – after a thorough peer-review process – about 270 papers for publication. The large number of papers accepted is certainly a proof of the vitality and attractiveness of the field of artificial neural networks, but it also shows a strong interest in the ICANN conferences.

Parallel Computing Technologies Victor Malyshev, 2009-09-01 The PaCT-2009 (Parallel Computing Technologies) conference was a four-day event held in Novosibirsk. This was the tenth international conference to be held in the PaCT series. The conferences are held in Russia every odd year. The first conference, PaCT 1991, was held in Novosibirsk (Academgorodok), September 7–11, 1991. The next PaCT conferences were held in Obninsk (near Moscow), August 30 to September 4, 1993; in St. Petersburg, September 12–15, 1995; in Yaroslavl, September 9–12, 1997; in Pushkin (near St. Petersburg), September 6–10, 1999; in Academgorodok (Novosibirsk), September 3–7, 2001; in Nizhni Novgorod, September 15–19, 2003; in Krasnoyarsk, September 5–9, 2005; in Pereslavl-Zalessky, September 3–7, 2007. Since 1995 all the PaCT Proceedings have been published by Springer in the LNCS series. PaCT-2009 was jointly organized by the Institute of Computational Mathematics and Mathematical Geophysics of the Russian Academy of Sciences (RAS) and the State University of Novosibirsk. The purpose of the conference was to bring together scientists working on theory, architecture, software, hardware and the solution of large-scale problems in order to provide integrated discussions on parallel computing technologies. The conference attracted about 100 participants from around the world. Authors from 17 countries submitted 72 papers. Of those submitted, 34 were selected for the conference as regular papers; there were also 2 invited papers. In addition there were a number of posters presented. All the papers were internationally reviewed by at least three referees. A demo session was organized for the participants.

Broca's Region Yosef Grodzinsky, Katrin Amunts, 2006-04-20 Broca's region has been in the news ever since scientists realized that particular cognitive functions could be localized to parts of the cerebral cortex. Its

discoverer, Paul Broca, was one of the first researchers to argue for a direct connection between a concrete behavior--in this case, the use of language--and a specific cortical region. Today, Broca's region is perhaps the most famous part of the human brain, and for over a century, has persisted as the focus of intense research and numerous debates. The name has even penetrated mainstream culture through popular science and the theater. Broca's region is famous for a good reason: As language is one of the most distinctive human traits, the cognitive mechanisms that support it and the tissues in which these mechanisms are housed are also quite complex, and so have the potential to reveal a lot not only about how words, phrases, sentences, and grammatical rules are instantiated in neural tissue, but also, and more broadly, about how brain function relates to behavior. Paul Broca's discoveries were an important, driving force behind the more general effort to relate complex behavior to particular parts of the cerebral cortex, which, significantly, produced the first brain maps. These early studies also, however, suffered from the use of crude techniques, definitions, and distinctions, as well as from ill founded and misdirected assumptions. Although much has been discovered since Broca's work, even today, these problems have not been completely solved. Nonetheless, particularly as a result of important advances made in neuroimaging during the past two decades, Broca's region and all language areas are currently being investigated from every angle. Indeed, as the volume of research into the relations between brain and language has created several communities, each with its own concepts, methods, and considerations, it seemed that it was time to stop, get together, and reflect on the state of the art. This book is the result of that collective reflection, which took place primarily at the Broca's Region Workshop, held in Jülich and Aachen, Germany, in June 2004. In it, Yosef Grodzinsky and Katrin Amunts tried to accomplish a nearly impossible task: to mix intellectual traditions and cultures, and juxtapose rather disparate bodies of knowledge, styles of reasoning, and forms of argumentation. Participants were scientists with diverse backgrounds; each invited to contribute his/her particular take, with the hope that a coherent, perhaps even novel, picture would emerge. All of the participants have a special interest in Broca's Region, and represent the myriad angles from which we currently approach it: neuroanatomy, physiology, evolutionary biology, cognitive psychology, clinical neurology, functional imaging, speech and language research, computational biology, and psycho-, neuro-, and theoretical linguistics. The book's main chapters are the contributions of the Workshop's participants and their research teams. Parts of the discussion during the Workshop are included to underscore the richness of viewpoints, and to give readers an idea of the level of interaction that took place. As Broca's region is such a historically significant concept and rich area, this book contains a collection of classic and recent-yet-classic papers. Along with cutting-edge science, Grodzinsky and Amunts want to remind readers of the celebrated past from which much can be learned. The historical chapters include the first two papers written by Paul Broca, as well some work by two of the most important neurologists of the nineteenth century, Ludwig Lichtheim and John Hughlings-Jackson. Also included are parts of twentieth century papers by Korbinian Brodmann, Roman Jakobson, Norman Geschwind, Harold Goodglass, and Jay Mohr. Because this book both reflects the state of the art in Broca's-region research and contains a tribute to its

celebrated past, it will be a valuable resource for student and professional researchers. It will also stimulate further interdisciplinary research, which is a significant contribution, as the project called Broca's region, encompassing the study of brain/language relations, is far from finished.

Multicore Technology Muhammad Yasir Qadri, Stephen J. Sangwine, 2013-07-26 The saturation of design complexity and clock frequencies for single-core processors has resulted in the emergence of multicore architectures as an alternative design paradigm. Nowadays, multicore/multithreaded computing systems are not only a de-facto standard for high-end applications, they are also gaining popularity in the field of embedded computing. The start of the multicore era has altered the concepts relating to almost all of the areas of computer architecture design, including core design, memory management, thread scheduling, application support, inter-processor communication, debugging, and power management. This book gives readers a holistic overview of the field and guides them to further avenues of research by covering the state of the art in this area. It includes contributions from industry as well as academia.

Neural Information Processing Tingwen Huang, Zhigang Zeng, Chuandong Li, Chi Sing Leung, 2012-11-05 The five volume set LNCS 7663, LNCS 7664, LNCS 7665, LNCS 7666 and LNCS 7667 constitutes the proceedings of the 19th International Conference on Neural Information Processing, ICONIP 2012, held in Doha, Qatar, in November 2012. The 423 regular session papers presented were carefully reviewed and selected from numerous submissions. These papers cover all major topics of theoretical research, empirical study and applications of neural information processing research. The 5 volumes represent 5 topical sections containing articles on theoretical analysis, neural modeling, algorithms, applications, as well as simulation and synthesis.

Modernizing Legacy Applications in PHP Paul Jones, 2014-05-02 This book will show you how to modernize your page-based, include-oriented PHP application by extracting and replacing its legacy artifacts. We will use a step-by-step approach, moving slowly and methodically, to improve your application from the ground up. Each completed step in the process will keep your codebase fully operational with higher quality. Please note that this book is about modernizing in terms of practice and technique, and not in terms of tools. We are not going to discuss the latest, hottest frameworks or libraries. Most of the very limited code we do add to your application is specific to this book. When we are done, you will be able to breeze through your code like the wind. Your code will be fully modernized: autoloaded, dependency-injected, unit-tested, layer-separated, and front-controlled.

Software Engineering and Formal Methods Gilles Barthe, Alberto Pardo, Gerardo Schneider, 2011-10-21 This book constitutes the refereed proceedings of the 9th International Conference on Software Engineering and Formal Methods, SEFM 2011, held in Montevideo, Uruguay, in November 2011. The 22 revised regular papers presented together with 1 short paper, 2 tool papers, and 4 keynote talks were carefully reviewed and selected from 105 initial abstracts and 85 full submissions. Besides the regular session the conference held a special track devoted to Modeling for Sustainable Development with 5 accepted papers - selected from 7 submissions - that are also part of this volume. The aim of SEFM is to advance the state of the art in formal methods, to scale up their

application in software industry and to encourage their integration with practical engineering methods.

SQL the One Uday Arumilli, 2016-12-17 Congratulations! You are going to WIN your next SQL Server interview. "SQL The One" book can guide you to achieve the success in your next interview. This book covers Microsoft SQL Server interview experiences, questions and answers for a range of SQL DBA's and SQL Server Professionals. All of these questions have been collected from the people who attended interviews at various multinational companies across the world. It also covers "How to prepare for a SQL DBA interview?" and "How to become an expert in your career?"
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High Speed Profiling Device Greg A. Larson, 1994

Modelling and Planning for Sensor Based Intelligent Robot Systems Horst Bunke, Takeo Kanade, Hartmut Noltemeier, 1995 This edited and reviewed volume consists of papers that were originally presented at a workshop in the Scientific Center at Schloss Dagstuhl, Germany. It gives an overview of the field and presents the latest developments in the areas of modeling and planning for sensor based robots. The particular topics addressed include active vision, sensor fusion, environment modeling, motion planning, robot navigation, distributed control architectures, reactive behavior, and others.

Transactional Memory, Second Edition Tim Harris, James Larus, Ravi Rajwar, 2022-05-31 The advent of multicore processors has renewed interest in the idea of incorporating transactions into the programming model used to write parallel programs. This approach, known as transactional memory, offers an alternative, and hopefully better, way to coordinate concurrent threads. The ACI (atomicity, consistency, isolation) properties of transactions provide a foundation to ensure that concurrent reads and writes of shared data do not produce inconsistent or incorrect results. At a higher level, a computation wrapped in a transaction executes atomically - either it completes successfully and commits its result in its entirety or it aborts. In addition, isolation ensures the transaction produces the same result as if no other transactions were executing concurrently. Although transactions are not a parallel programming panacea, they shift much of the burden of synchronizing and coordinating parallel computations from a programmer to a compiler, to a language runtime system, or to hardware. The challenge for the system implementers is to build an efficient transactional memory infrastructure. This book presents an overview of the state of the art in the design and implementation of transactional memory systems, as of early spring 2010.
Table of Contents: Introduction / Basic Transactions / Building on Basic Transactions / Software Transactional Memory / Hardware-Supported Transactional Memory / Conclusions

Compiler Construction Oege de Moor, Michael I. Schwartzbach, 2009-03-09 This book constitutes the refereed proceedings of the 18th International Conference on Compiler Construction, CC 2009, held in York, UK, in March

2009 as part of ETAPS 2009, the European Joint Conferences on Theory and Practice of Software. Following a very thorough review process, 18 full research papers were selected from 72 submissions. Topics covered include traditional compiler construction, compiler analyses, runtime systems and tools, programming tools, techniques for specific domains, and the design and implementation of novel language constructs.

Modern Compiler Implementation in ML Andrew W. Appel, 2004-07-08 This new, expanded textbook describes all phases of a modern compiler: lexical analysis, parsing, abstract syntax, semantic actions, intermediate representations, instruction selection via tree matching, dataflow analysis, graph-coloring register allocation, and runtime systems. It includes good coverage of current techniques in code generation and register allocation, as well as functional and object-oriented languages, that are missing from most books. In addition, more advanced chapters are now included so that it can be used as the basis for two-semester or graduate course. The most accepted and successful techniques are described in a concise way, rather than as an exhaustive catalog of every possible variant. Detailed descriptions of the interfaces between modules of a compiler are illustrated with actual C header files. The first part of the book, Fundamentals of Compilation, is suitable for a one-semester first course in compiler design. The second part, Advanced Topics, which includes the advanced chapters, covers the compilation of object-oriented and functional languages, garbage collection, loop optimizations, SSA form, loop scheduling, and optimization for cache-memory hierarchies.

Psychology of Learning and Motivation , 1970-04-01 Psychology of Learning and Motivation

Distributed Computing Shlomi Dolev, 2006-10-05 This book constitutes the refereed proceedings of the 20th International Symposium on Distributed Computing, DISC 2006. The book presents 35 revised full papers together with 1 invited paper and 13 announcements of ongoing works, all carefully selected for inclusion in the book. The entire scope of current issues in distributed computing is addressed, ranging from foundational and theoretical topics to algorithms and systems issues and to applications in various fields.

Networked Systems Andreas Podelski, François Taïani, 2019-01-05 This book constitutes the thoroughly refereed conference proceedings of the 6th International Conference on Networked Systems, NETYS 2018, held in Essaouira, Morocco, in May 2018. The 22 full and 6 short papers presented together with 11 keynotes and 2 invited papers were carefully reviewed and selected from 85 submissions. They are organized in the following topics: distribution; concurrency; verification; networking; self-stabilization; security; graph; and middleware.

Proceedings of International Conference on Frontiers in Computing and Systems Debotosh Bhattacharjee, Dipak Kumar Kole, Nilanjan Dey, Subhadip Basu, Dariusz Plewczynski, 2020-11-23 This book gathers outstanding research papers presented at the International Conference on Frontiers in Computing and Systems (COMSYS 2020), held on January 13–15, 2019 at Jalpaiguri Government Engineering College, West Bengal, India and jointly organized by the Department of Computer Science & Engineering and Department of Electronics & Communication Engineering. The book presents the latest research and results in various fields of machine learning, computational intelligence, VLSI, networks and systems, computational biology, and security, making it a rich source of reference material for

academia and industry alike.

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