

Coradrive&sorty

J Rink

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time intervals

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that an 8051

microcontroller can

access for ram and rom

is 64kb 2 for each type

additional features

interrupts on chip

oscillator boolean

processor power down

mode etc some of the

features like size of

ram and rom number of

timers etc are not

generic they vary by

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general schematic

diagram of 8051

microcontroller is shown

above we can see 3

system inputs 3 control

signals and 4 ports for

external interfacing a

vcc power supply and

ground is also shown now

lets explain and go through each in detail system inputs are necessary to make the micro controller functional

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